C16-EE-505

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BOARD DIPLOMA EXAMINATION, (C-16)

MARCH/APRIL—2021

DEEE - FIFTH SEMESTER EXAMINATION

Instructions:

- Answer altiquestions.

 Each question carries three marks.

 Answers should be brief and straight and exceed five simple sentence.

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- 3.4
- **&** 4.

 - 6.
 - 7.

 - 9.

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Instructions: (1) Answer any **five** questions.

- (2) Each question carries ten marks.
- Draw and explain AND, OR, NOT, NAND, NOR and EX-OR gates 11.
- Draw the full-adder circuit using on the NANI operation with truth table.

 Draw and explain 3 × 8 decider.

 Draw and explain the Norking 4-bit bi-direction of the NANI operation with truth table. **12**.
- 13.
- orking 4-bit bi-directional shift register. 14.
- 15.

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