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BOARD DIPLOMA EXAMINATION, (C-16)

JANUARY/FEBRUARY-2022

DECE - FOURTH SEMESTER EXAMINATION

MICROPROCESSORS

Time: 3 hours]

PART—A

[Total Marks : 80

3×10=30

- **Instructions :** (1) Answer **all** questions.
 - (2) Each question carries **three** marks.
 - (3) Answers should be brief and straight to the point and shall not exceed five simple sentences.
 - **1.** Define fetch cycle, execution cycle and instruction cycle.
 - 2. List any three different registers of 8085 Microprocessor.
 - **3.** State the importance of instruction pointer of 8086 Microprocessor.
 - **4.** List any three types of flags of 8086 Microprocessor.
 - **5.** Write the generalized instruction format of 8086 Microprocessor.
 - 6. List any three processor control instruction of 8086 Microprocessor.
 - 7. List any three assembler directives.
 - **8.** State the need of subroutine.
 - **9.** State any three features of 80286 Microprocessor.
 - **10.** Define pipe lining.

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PART—B

Instructions :				(1)	Answer any five questions.	
				(2)	Each question carries ten marks.	
				(3)	Answers should be comprehensive and criterion for valuation is the content but not the length of the answer.	
11.	(a)	D	rav	v tł	ne pin out diagram of 8085.	4
	(b) Give the syntax and function of STA, LDA and IN instruct 8085.					6
12.	Draw and explain functional block diagram of 8086.					
13.	Explain the interrupt response of 8086.					10
14.	Explain any five addressing modes of 8086 Microprocessor with examples.					10
15.	Explain any five data transfer instruction with example of 8086.					
16.	Write assembly language program to find smallest number of the given series 5, 6, 7, 3, 9, 4, 8.					
17.	Explain memory organization in 80386 Microprocessor.					10
18.	Exp	ola	in	ins	truction level parallelism.	10
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