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BOARD DIPLOMA EXAMINATION
JUNE - 2019
DIPLOMA IN COMPUTER ENGINEERING
MICROPROCESSORS
FOURTH SEMESTER EXAMINATION

Time: 3 Hours

Total Marks: 80

PART - A (3m x 10 = 30m)

Note 1: Answer all questions and each question carries 3 marks

2: Answers should be brief and straight to the point and shall not exceed 5 simple sentences

1. Define fetch cycle, execution cycle and instruction cycle?
2. Draw the timing diagram of memory read cycle?
3. Explain the following instructions?
 a) PUSH b) ADD c) CMP
4. Write about any three Logical instructions
5. . Write short notes on interrupts
6. What is use of assembly language Programming
7. List the types of call instructions in 8086
8. List the any 6 features of 80286
9. Write the details of 8051 family chips
10. Draw the formats of S_{CON}, P_{CON}?

PART - B (10m x 5 = 50m)

Note 1: Answer any five questions and each carries 10 marks

2: The answers should be comprehensive and the criteria for valuation is the content but not the length of the answer

11. Explain string manipulated instructions of 8086
- * 12. Define addressing mode? And explain the different addressing modes of 8086
13. Explain the interrupt handling process in 8086
14. Explain different steps involved in executing an assembly language program with assembler
- 15A. Explain the bus cycles and timing diagram of 8086
- B. Explain briefly about operation of Programmable interrupt controller

16. Explain the architecture of Pentium microprocessor.
17. Draw the block diagram of micro-computer and explain the function of each block?
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18. Explain about various special functional registers.

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