QUESTION PAPER - 2016 (T.S)

COMPUTER SCIENCE & ENGINEERING

(10) 2 -

$\mathbf{Q.1}$	The class which is inherited by parent class is termed as				£ .	3	(21)
	(1) base class		(2) deriv	ed class		¢	17.11
	(3) member of class	/cc.		c membe	er of class		J. F.O.
Q.2	A pointer to the base class	can hold addres	ss of			À	
	(1) only base class object	(72)		(85)		1/2	(C X)
	(2) only derived class obje	ect [8]	. L.	(1)(1)		8	
	(3) base class object as we	ell as derived cla	ss object	(84)			(33)
	(4) only base class		4	(88)			(7.7)
$\mathbf{Q.3}$	Consider the following state	ements:					
	int $x = 22$, $y = 15$;						
	x = (x>y) ? (x+Y) : (x-y);			() (1)			7040
	What will be the value of x	after executing	these state	ements		P.	(64)
	(1) 22	1844	(2) 37	(54)			(53)
	(3) 7	(920)	7	1001	be executed	2	[57]
Q.4	Which of the following ope	rator can be ove	rloaded th	rough fr		n	[6]
	(1)> $(2) =$		(3) (-)		(4) *	Ç.	
Q.5	What would be the output	of the following	program		-	0	
	iont main()	(1)	17	(U/)	•	3	
	$\{ \text{ intx,y} = 10, z = 10 \}$		I	(47)		4	(73)
	x = (y==z);cout < < x;	1-11	.54	125		C,	1
	return 0;}				•	2	
	(1) 0 (2) 1		(3) synta				
Q.6	In C++, dynamic memory		10 m	- //-			
%£	(1) new (2) this		(3) size c		(4) delete		
Q.7	Which class does not over	ride the equals () and hash	n code() i	methods, inh	neri	ting them
	directly from class object		(2) invo	lana atuir	. ~		
	(1) java.lang.string Buffer		(2) java.	•	_		
0.0	(3) java.lang.double	. f	(4) java.	•			
Q.8	Java language has support						
	(1) block, line and javadoo				al and string	•	
	(3) javadoc, char and string (4) single, multiple and quote						

Q.9	Which of the following statement is correct	Milypanyoli Musika in ilipaka Milinisi i iki 🖟				
	(1) for positive two numbers, result of operators $>>$ and $>>>$ are same					
	(2) java provides two operators to do left s	shift <<< and <<				
	(3) >> is the zero fill right shift operator	nto stasupez paiweltal and a die EnQ				
	(4) >>> is the signed right shift operator	(Stenie Liberte Liber				
Q.10	What is the output of the given java code	snippet class c 1 {public static void main				
	(string a []) {	is in the Opensylvanian in a provinced of the				
	c 1 ob 1 = new c 1 ();					
	object ob $2 = ob 1$;	instruction with a state of the				
	system.out.print1n(ob2 instance of object);					
	system.out.print1n(ob2 instance of c 1);}}	en delad dia sympa llog off to it aliai 81 b				
	(1) true, false (2) false, true	(3) true, true (4) compile time error				
Q.11	What is the output of the given java code s	nippet class bike{}				
	class arr extends bike {					
	<pre>public static viod main(string[]args) {</pre>					
	arr[]a1 = new arr[2];					
	bike[]a 2;					
	a 2 = a 1;					
	arr[]a3;					
	a3 = a1	ed for more and the family of the family of the				
	system.out.print1n(a3);}}	da de pata de la compania del compania de la compania del compania de la compania del la compania de la compania del la compania de la compan				
	(1) compile time error at line 3	(2) compile time error at line 5				
	(3) runtime exception	(4) garbage value				
Q.12	What is the output of the given java code s	nippet				
	class C{	hrine a sour Bhash tu starti ania (4)				
	public static void main (string [] args 0{	TEA DE MARIA DE DESTRUCCION OF BRAC				
	byte b1 = 33; $/\!/1$	Loimenten SMEG (f)				
	b1++; //2	· Istalesagus APA (Er				
	byte $b2 = 55$; //3	122 Which layer a C. 13 Hetes weens				
	b2 = b1 + 1; //4					
	system.out.print1n(b1+""+b2);					
	}}					
	(1) compile time error at line 2	(2) compile time error at line 4				
	(3) 34, 56					
Q.13						
	(1) memory bound exception	(2) out of memory error				
	(3) out of range exception	(4) null reference exception				

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Q.14	4 Which exception is thrown by red() method					
	(1) exception	(2) file not found exception				
	(3) read exception	(4) IO exception				
Q.15	Which of the following sequence of method	od calls take place when an applet begins				
	(1) init(), start(), create()	(2) start(), init(), paint()				
MAM	(3) init(), start(), paint()	(4) start(), paint(), destroy()				
Q.16	On invoking repaint() method for a comp	onent, which method is invoked by AWT				
	(1) draw() (2) show()	(3) update() (4) paint()				
Q.17	A file that specifies how the screen is divi	ded into frames is called as				
	(1) frame table (2) table link	(3) framediv (4) frameset				
Q.18	Which of the following tag helps to add document	a paragraph break after the text in HTML				
	(1) <paragraph></paragraph>	(2) <p></p>				
	(3) 	(4) <nextline></nextline>				
Q.19	How to define target in new page in HTM					
	(1) <a href="http://com/" target="l</th><th>olank"> click Here					
	(2) \langle a href = "http://com/" target = "l	olank"> click Here				
	(3) <a "="" com="" href="http//com/" http:="" target=":</th><th>#blank"> click Here					
Q.20	XLL definition is used along with XML to					
	(1) data type of the contents of XML document					
	(2) presentation of XML document	noitasoks smilling (E)				
	(3) links with other document	Q.12 What is the output of the given rava co				
	(4) structure of XML document	class CI				
Q.21	To connect database to ASP, object	is created in the first step				
	(1) DBMS connention	(2) ADO connection				
	(3) ASP connection	(4) ODBC connection				
Q.22	Which layer of the OSI reference model ha	andles flow control and error recovery				
	(1) application layer	(2) presentation layer				
	(3) transport layer	(4) network layer				
Q.23	SMTP is a					
	(1) networking protocol	(1) compule time error at ime 2				
	(2) protocol used for transferring message between and user & mail server					
	(3) protocol used for smart card message i	interchange MVI and and ELO				
	(4) encryption standard	(1) memory bound exception				

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(4) null reference exception

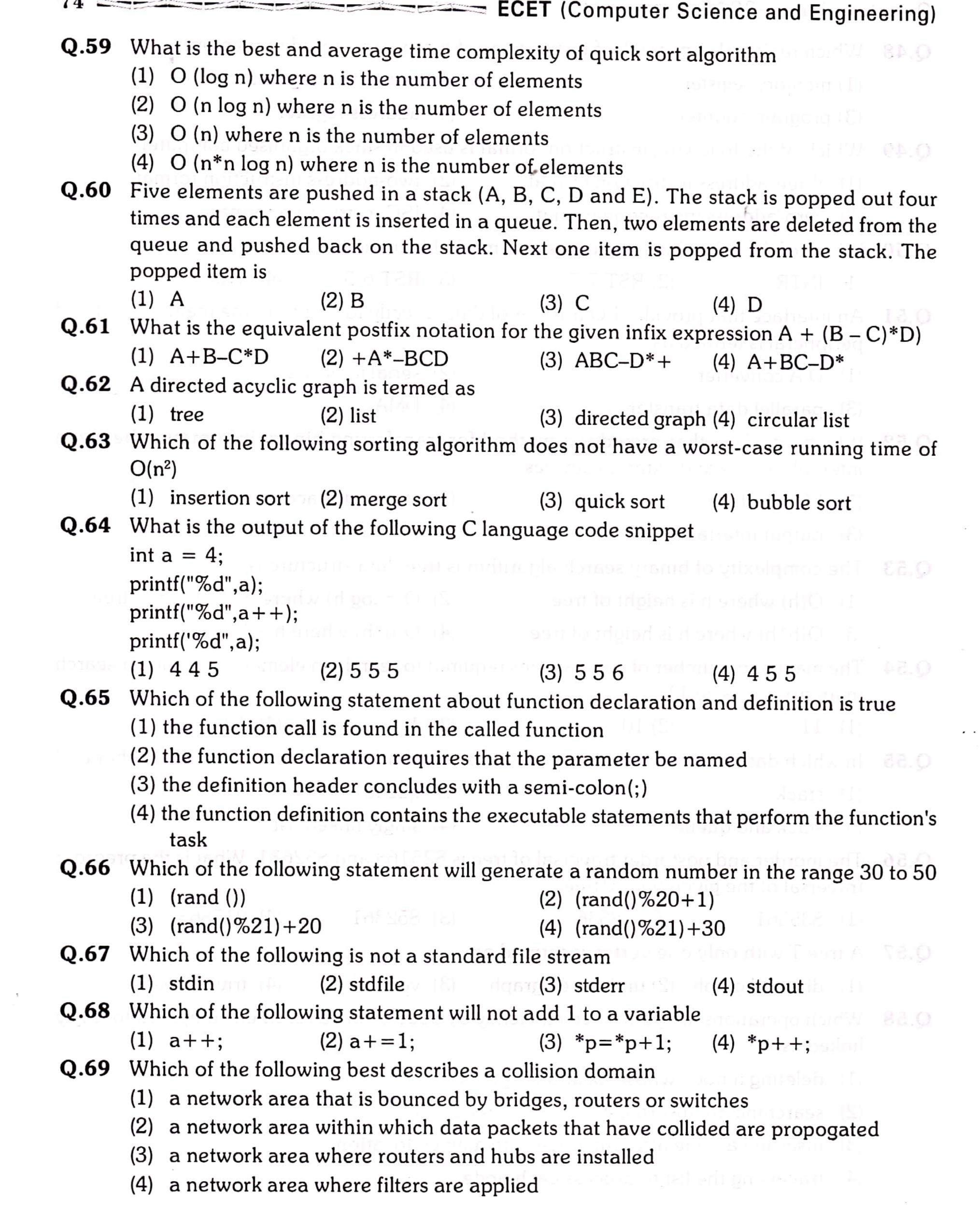
Quest	tion Paper-2016					
Q.24	LDAP stands for					
Q.2 4						
	(1) light weight data access protocol(2) light weight directory access protocol					
	(3) large data access protocol					
	(4) large data access protocol					
Q.25						
Q.2 3	(1) (AND OD) (2) (NAME NOTE)					
O 26	(1) {AND, OR} (2) {NAND, NOR}	(3) {NAND} (4) {NOR, NOT}				
Q.20	Which property holds true for NAND and N (1) both associative and commutative					
		(2) commutative only				
O 27	(3) associative only	(4) transitive only				
Q.27		mum number of literals for the given function				
	F(x, y, z) = x'y'z + xy'z + xyz	(O)				
	(1) xy' + xz	(2) $y'z + x'y'z + xyz$				
O 26	(3) xyz + x'y' Which of the following is I down	(4) $xyz + x'y'z$				
Q.28	Which of the following is I dempotence lav	(0)				
0.00	(1) $x+xy=x$ (2) $x(x+y)=x$	(3) $x+x=x$ (4) $1+x=1$				
Q.29	To design 8×1 multiplexer using 2×1 multiplexer only, how many 2×1 multiplexer are required					
	(1) 7 (2) 6	(3) 5 (4) 4				
Q.30	How many number of boolean function ca	n be formed with 3-variables				
	(1) 8 (2) 16	(3) 256 (4) 32				
Q.31	To design a counter for the sequence $1, 2, 3, 4, 5, 6, 7, 8, 1, 2, 3, \ldots$, how many flip-flops are required					
	(1) 3 (2) 4	(3) 5 (4) 2				
Q.32	How many numbers of bits are required to code the 26 alphabets, 10 digits and 10 special characters					
	(1) 6 (2) 5	(3) 4 (4) 16				
Q.33	The systematic reduction of logic circuits i	• • • • • • • • • • • • • • • • • • • •				
	(1) using boolean algebra	(2) symbolic reduction				
	(3) TTL logic	(4) using a truth table				
Q.34	How many numbers 1's are present in the binary representation of : $15 \times 256 + 5 \times 16 + 3$					
	(1) 2 (2) 5	(3) 10 (4) 8				
Q.35	Which one of the following boolean expressions is not logically equivalent to all other expressions					
	(1) wxy' + wz' + wxyz + wy'z	(2) $wx + wy' + wyz'$				
	(3) $wx + wy' + wz'$	(4) $w + x + y' + z'$				

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Q.36	What is the additional logic required to convert D flip-flop into T flip-flop						
	$(1) D = T.Q_n$	(2) $D = T$	$(3) D = T + Q_n$	$(4) D = T \oplus Q_n$			
Q.37	Which segment region 8086 processor a		register during string manipulations instructions				
	(1) extra segment	(2) code segment	*(3) stack segmen	nt (4) data segment			
Q.38	Which flag acts as	borrow flag for SBB ins	struction in 8086 a	rchitecture			
	(1) auxillary flag	(2) carry flag	(3) parity flag	(4) trap flag			
Q.39			ction sequence giv	ven below will loop before			
	coming out of loop	ditatummon (S) e	and commutativ	Liboth associativ			
		no avinensu (A)		(3) associative only			
	A1 : INCAL JZNAI	'lo indinui muninim		Q.27 What is the simplified by $2x + y = x' + y$			
	(1) 00	(2) 01	(3) 255	(4) 256			
Q.40	Which microproces	ssor pins are used to rec	quest and acknowle	edge a DMA transfer			
	(1) reset and ready	J	(2) ready and wa	oit Which of the follow			
	(3) HOLD and HL	.DA	(4) BSR and BS				
Q.41	How many number	of address lines are re	quired to address a	a memory of size 32K			
	(1) 15 lines	(2) 16 lines	(3) 18 lines	(4) 14 lines			
Q.42	What is the size of	each segment in 8086	21.6				
	(1) 64 KB	(2) 24 KB	(3) 50 KB	(4) 16 KB			
Q.43	•		17. 12.1	nt representation. What is			
-qift vi		al values, n, that can b	A STATE OF A STATE OF THE PARTY OF	Q.31 To design a payme			
	$(1) -2^{N-1} < n < 2^{N}$		$(2) -2^{N-1} < n <$				
0.44	$(3) -2^{N-1} <= n < 3$		$(4) -2^{N-1} < n < 1$	((1)			
Q.44		syste, cache access ting is 100 ns. What is the		s rate is 50% and the main			
	(1) 35 ns			(4) 110 ns			
0.45							
	5 A computer system stores floating-point numbers with a 16-bit mantissa and an exponent, each in two's complement form. What is the smallest and largest post values that can be stored in this system						
	(1) 1×10^{-128} and	$2^{15} \times 10^{128}$	(2) 1×10^{-256} ar	$10^{15} \times 10^{255}$			
	(3) 1×10^{-128} and	$2^{15} \times 10^{127}$	(4) 1×10^{-128} ar	nd $(2^{15}-1) \times 10^{127}$			
Q.46	In which memory v	vrite through technique	is used for updati	ng data			
	(1) cache memory	13.1 (8)	(2) auxillary mer	nory			
T9d10 J	(3) virtual memory	albaj tou si suoissaudxa	(4) secondary m	emory			
Q.47	· · · · · · · · · · · · · · · · · · ·	the state of the state of Co.		ess part of the instruction at is the effective address			
	(1) 85 AC	(2) 85 A 1	(3) 86 CF	(4) 86 DI			

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Q.48	Which register keeps track of instructions of a program stored in memory				
	(1) memory register (2) instruction register	register			
	(3) program counter (4) address register				
Q.49	Which of the following instruction format is used in stack organised computer				
	(1) three-address instruction format (2) two-address instruction format	* 1			
	(3) zero-address instruction format (4) ISC instruction format				
Q.50	Which of the following interrupt is non-maskable interrupt				
	(1) INTR (2) RST 7.5 (3) RST 6.5 (4) TRAP				
Q.51	An interface that provides I/O transfer of data directly to and from the memory unit a peripheral is termed as	nc			
	(1) D/A converter (2) serial interface	1			
	(3) parallel data transfer (4) DMA	•			
Q.52	Which interface that provides a method for transferring binary information between internal storage and external devices	er			
	(1) I/O interface (2) input interface				
	(3) output interface (4) I/O bus				
Q.53	The complexity of binary search algorithm is tree data structure is				
	(1) O(h) where h is height of tree (2) O(n log h) where h is height of tree				
	(3) $O(h*h)$ where h is height of tree (4) $O(n*h)$ where h is height of tree				
Q.54	The maximum number of comparisons required to search an element using linear sea				
	in an array of size 11	7			
	(1) 11 (2) 10 (3) 1 (4) 12				
Q.55	In which data structure, an insertion can perform at one end and deletion at other en	ıd			
	(1) stack				
	(3) stack and queue (4) singly linked list				
Q.56	The inorder and postorder traversal of tree is 825163 and 852631. What is the preord traversal of the given binary tree				
	(1) 835361 (2) 128536 (3) 852361 (4) 215863				
Q.57	A tree T with only one vertex is termed as	,			
	(1) directed graph (2) undirected graph (3) vertex tree (4) trivial tree				
Q.58	Which operations are performed efficiently by doubly linked list when compared to single linked list	y			
40	(1) deleting a node whose location is given	, -			
	(2) searching an unsorted list for a given element				
	(3) inserting a node after the node with a given location				
	(4) traversing the list to process each node				

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(3) where shared resources are accessed

(4) which must be enclosed by a pain of semaphores operation

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Q.79	Which of the following memory allocation scheme suffers from external fragmentation						
	(1) paging (2) segmentation						
usable	(3) swapping	(4) pure demand	paging	rk is subnette	lass C netwo	o.FI If a	
Q.80	Consider a job scheduling problem with 4 jobs J1, J2, J3, J4 and corresponding deadlines						
	(d1, d2, d3, d4) = (n of the fol	lowing is not a	feasible sche	dule without	
Pit 3	violating any job dead line (1) J2, J4, J1, J3 (2) J4, J1, J2, J3 (3) J4, J2, J3, J1 (4) J4, J2, J1, J3						
Q.81	1500120 J						
	(1) page fault is hardware fault and cache miss is software fault						
	(2) page fault is sof	and the base of the later of th		the state of the state of	The state of the state of	Q.73 At W	
	(3) page fault and o				hysical and a		
	(4) page fault is ger	erated cache mis	SS	ata link layer	hysical and d		
Q.82	Which one of the fo	llowing CPU sche	eduling alg	orithm leads t	o starvation p	oroblem	
	(1) FIFO	(2) round robin	(3)	SJF	(4) preempt	tive	
Q.83	To avoid the race co	ndition, how mar	ny number	of processes the	nat may be sir	nultaneously	
	running in the critic	al section			ignighty ov		
	(1) one	(2) two	- 7 to 1901 5 L	three	(4) four	Q.75 The	
Q.84	The process of swit				es to save sta	te of the old	
	process and loading	7 4 1 7 12		TDA 21 2 LPA	st out (FIFO)	rif ar	
0.05	(1) process blocking			thrashing	(4) polling		
Q.85	Which scheduling p	L 1 '1		-		m (I)	
militino	(1) shortest-job first(3) round-robin	id civica amii bu		priority-based first-come-first	ristre Transporad	Q.76 Ano	
Q.86	An operating system	contains 3 user	THE CHARLES			ource R. The	
Q.00	mininmum number					ource II. The	
	(1) 4	(2) 3	(3)	_	(4) 6	191	
Q.87	CPU burst time indi	cates the time, th	e process i	needs the CPU	. The followin	ng are the set	
	of processes with th	eir respective CP	U burst tin	ne (in milliseco	onds)	(c)	
	processesCPU-burst	time					
	P1	10	r process	valuing nine fo	is the total t	вdW	
	P2 33 (4)	5 (2)			3.1		
č5 9050		256 Mwords and					
	What is the average	waiting time if th	ne process	arrived in the	following ord	er : P2, P3 &	
	P1 (4) (4)	(3) 130 K	(2)	(2) 120 K	128 K		
A 88	(1) 5 Program 'programation	(2) 4	(3)	20 2 manperg 5 2	(4) 10	Q.78 A cm	
Q.88	Program 'preemptio	amount in tourners	n specified	un in a certair	r bluodit doid	the CDU	
	(1) forced de alloca			44	executing on	THE CPU	
(2) release of CPU by the program after completing its task (3) forced allotment of CPU by a program to itself						20 (A)	
	(3) forced allotment of CPU by a program to itself(4) a program terminating itself due to delection of an error						
	(1) a program term	mating itself due	to defection	in or an enor			